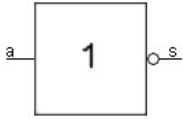
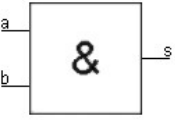


Truth tables (Logic gates):

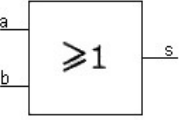
Logic gate NO:

	a	Output NO
	0	1
	1	0

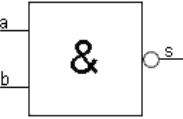
Logic gate AND:

	a	b	Output AND
	0	0	0
	0	1	0
	1	0	0
	1	1	1

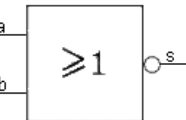
Logic gate OR:

	a	b	Output OR
	0	0	0
	0	1	1
	1	0	1
	1	1	1

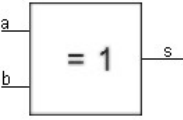
Logic gate NAND (NO AND):

	a	b	Output NAND
	0	0	1
	0	1	1
	1	0	1
	1	1	0

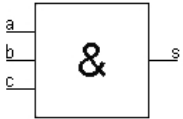
Logic gate NOR (NO OR):

	a	b	Output NOR
	0	0	1
	0	1	0
	1	0	0
	1	1	0

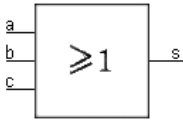
Logic gate XOR (Exclusive OR):

	a	b	Output XOR
	0	0	0
	0	1	1
	1	0	1
	1	1	0

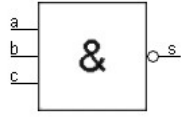
Logic gate AND with 3 inputs:

	a	b	c	Output AND (3 inputs)
	0	0	0	0
	0	0	1	0
	0	1	0	0
	0	1	1	0
	1	0	0	0
	1	0	1	0
	1	1	0	0
	1	1	1	1

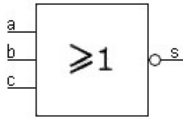
Logic gate OR with 3 inputs:

	a	b	c	Output OR (3 inputs)
	0	0	0	0
	0	0	1	1
	0	1	0	1
	0	1	1	1
	1	0	0	1
	1	0	1	1
	1	1	0	1
	1	1	1	1

Logic gate NAND (NO AND) with 3 inputs:

	a	b	c	Output NAND (3 inputs)
	0	0	0	1
	0	0	1	1
	0	1	0	1
	0	1	1	1
	1	0	0	1
	1	0	1	1
	1	1	0	1
	1	1	1	0

Logic gate NOR (NO OR) with 3 inputs:

	a	b	c	Output NOR (3 inputs)
	0	0	0	1
	0	0	1	0
	0	1	0	0
	0	1	1	0
	1	0	0	0
	1	0	1	0
	1	1	0	0
	1	1	1	0